IN THE SPECIFICATION

Please replace paragraph [0172] with the following marked up paragraph:

[0172] Referring to Figure 22B, an address generated by a context model, addr, accesses memory array 2201 generating a bit. The address and the counter value associated with the row of memory containing the address are input to comparison logic 2210. If comparison logic 2210 determines that the address is greater than or equal to the counter value for the row, then the 1-bit output from memory array 2201 is output; otherwise, a zero is output. The comparison indicates to the context model whether or not the bit of data is to be used by the context model.